

## IN THE DRAWINGS

The attached drawing sheets include a change to Figure 2. Figure 2 has been replaced by Figure 2A and Figure 2B. It is respectfully submitted that the proposed replacement to the drawing does not add new matter.

Attachment: Replacement Sheets  
Annotated Sheet Showing Change

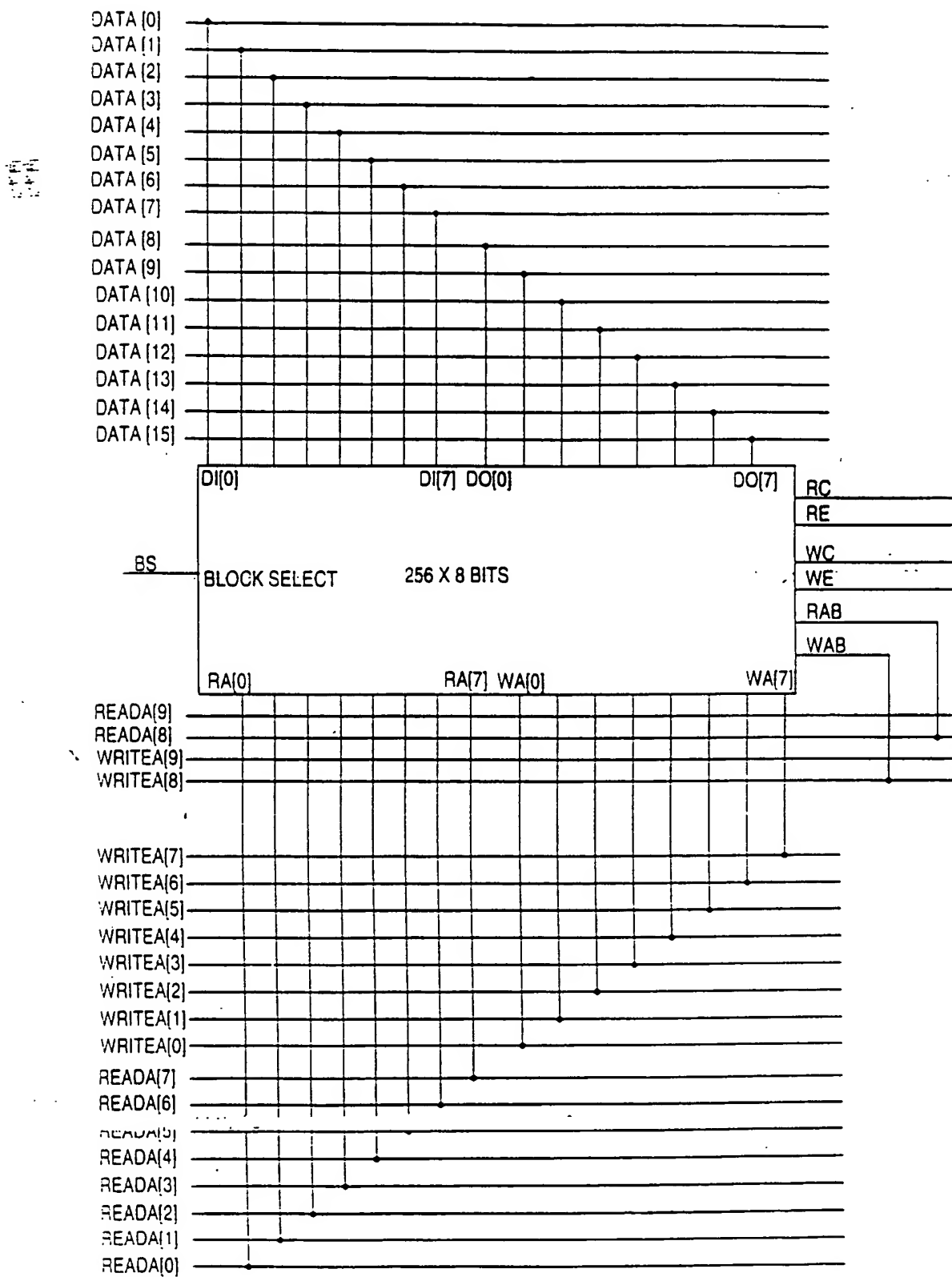


FIG. 1A (PRIOR ART)

FIG. 1B (PRIOR ART)

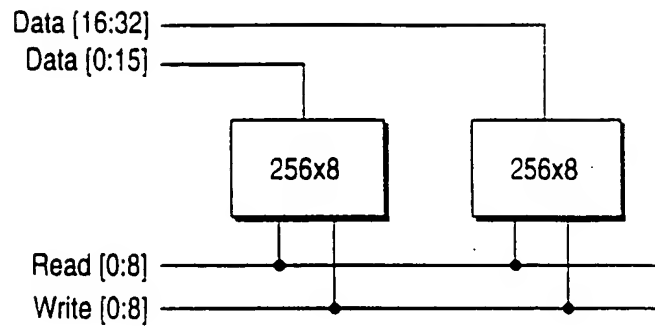
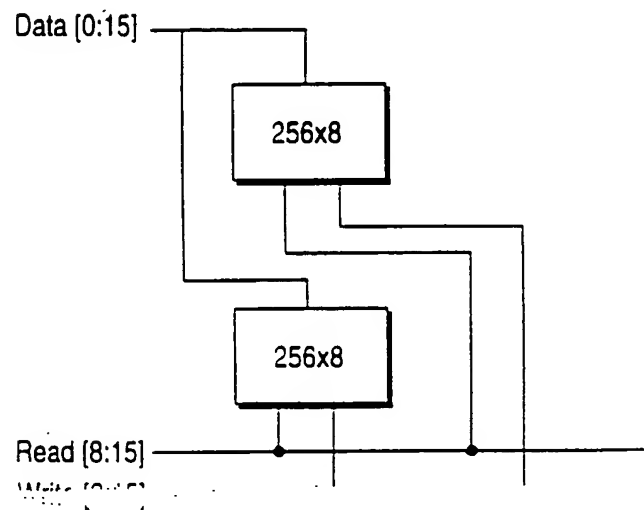


FIG. 1C (PRIOR ART)



*This sheet  
 has been  
 replaced with  
 figures 2A &  
 2B.*

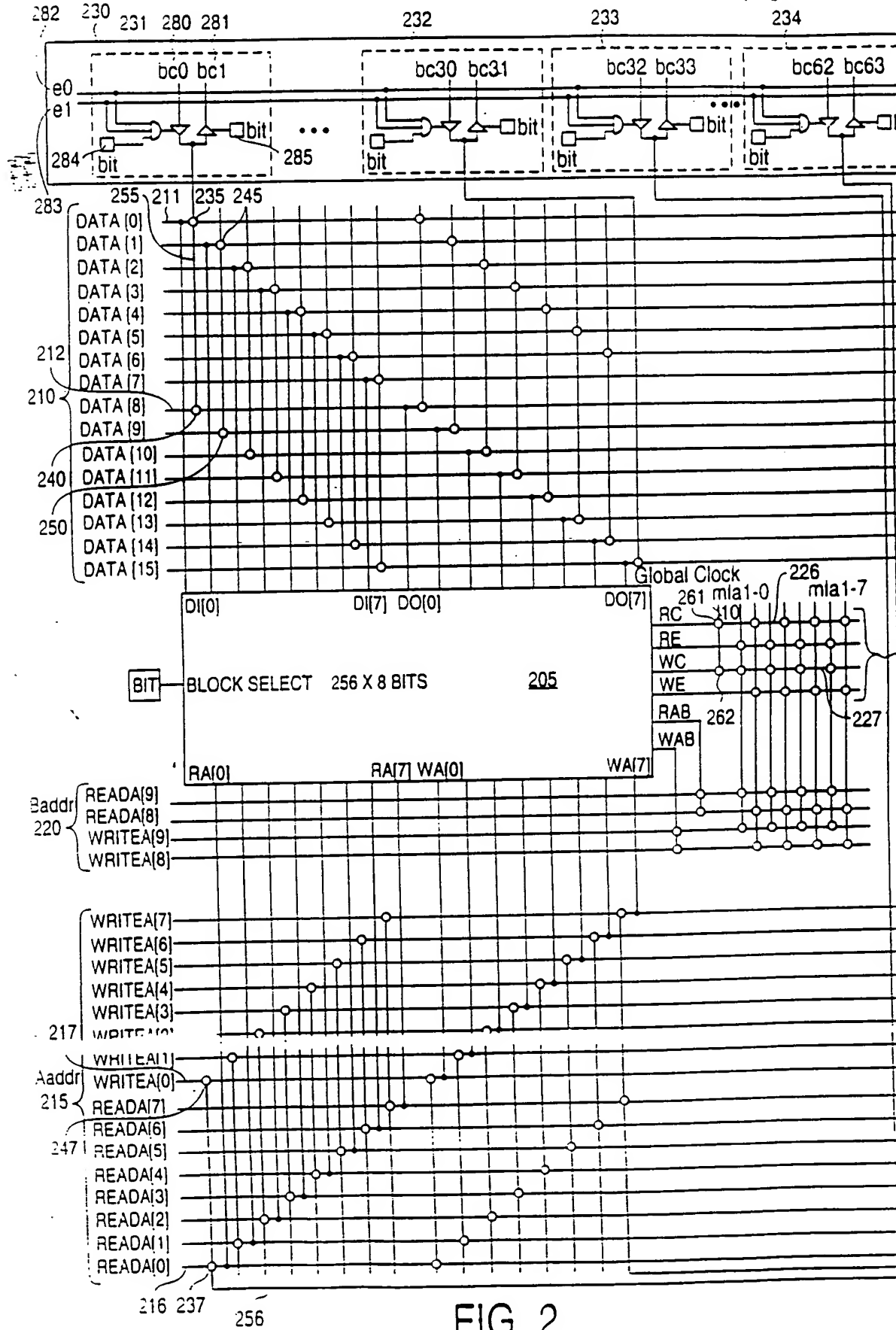


FIG. 2

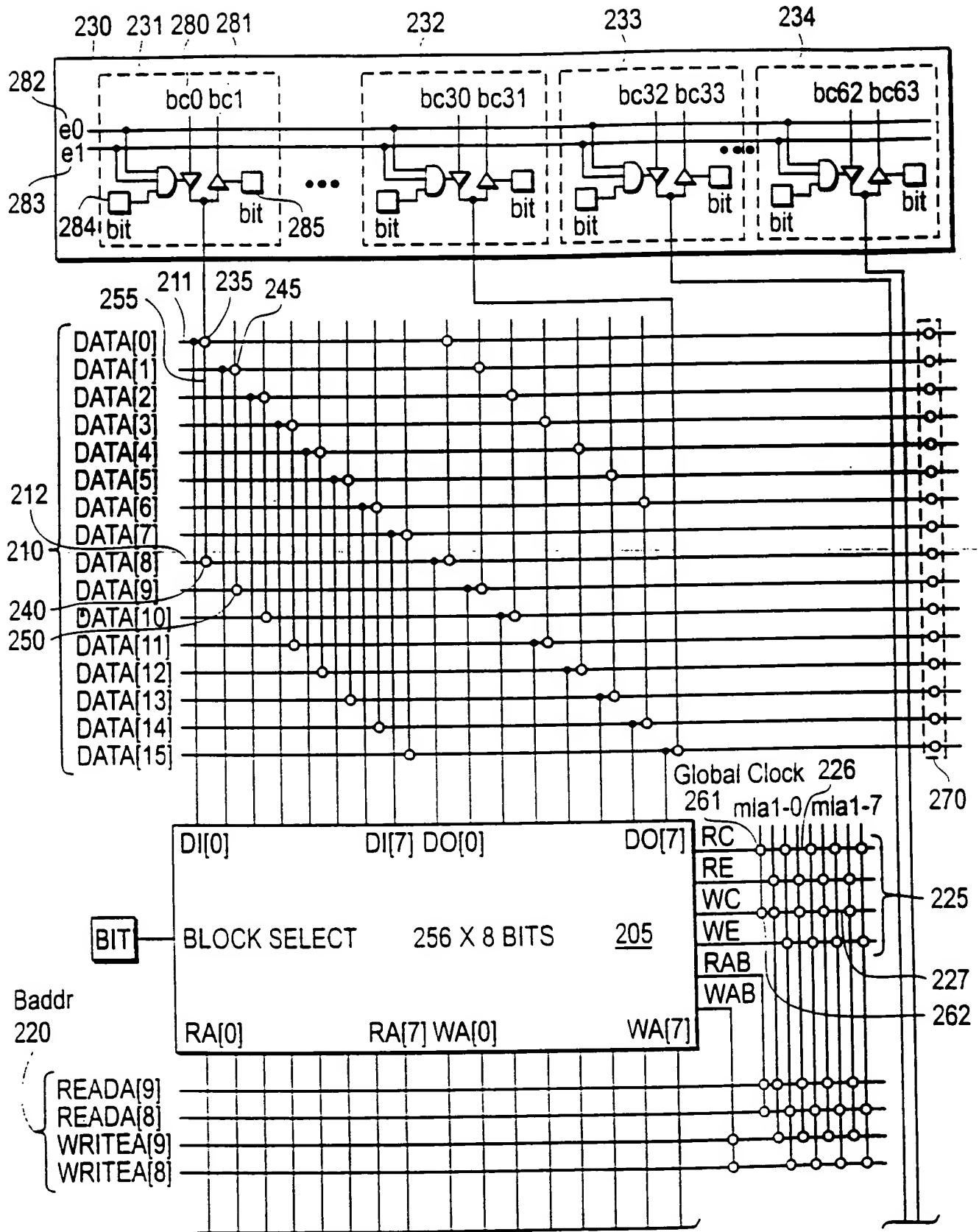


FIG. 2B ↓

FIG. 2A

FIG. 2A ↑

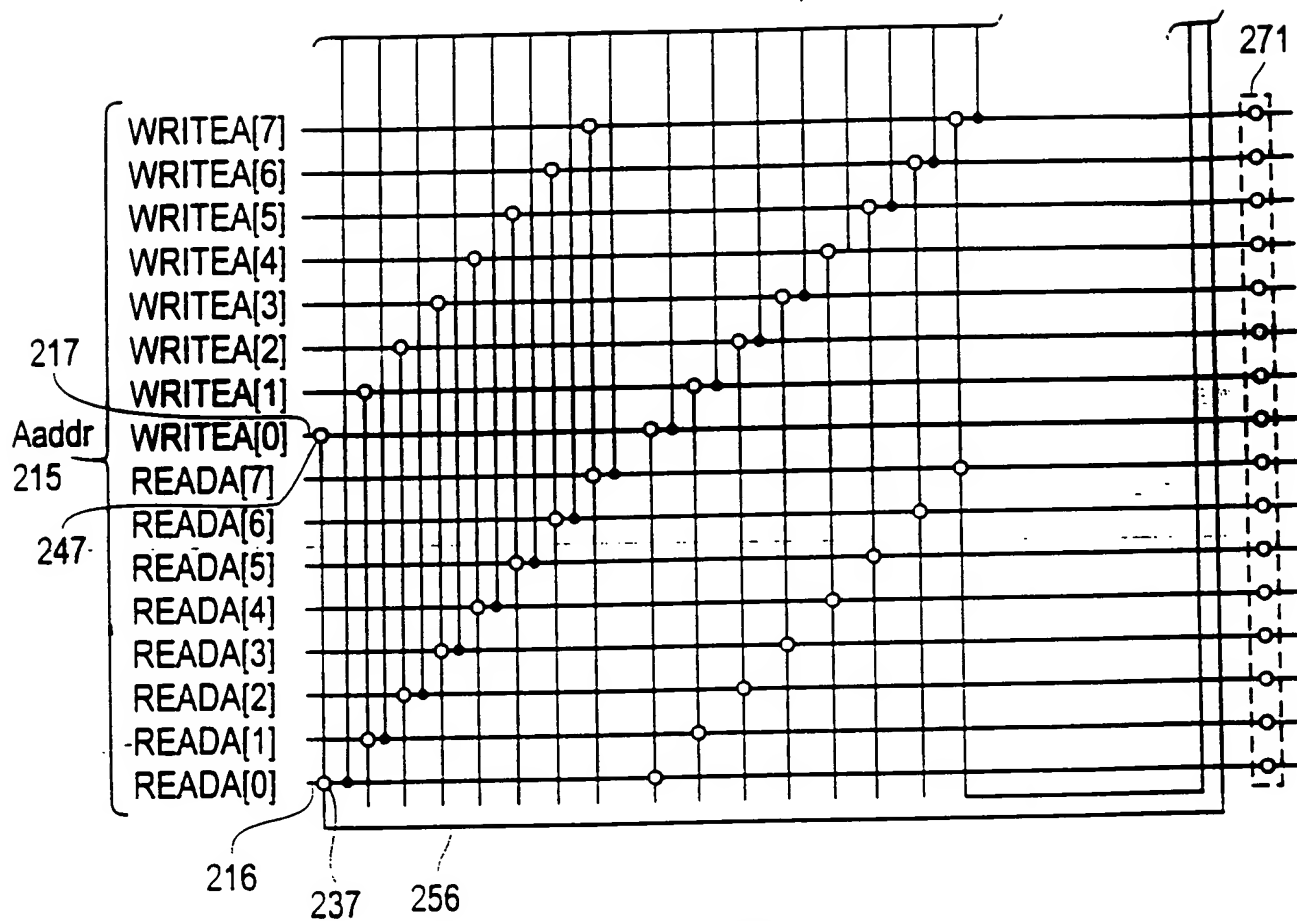


FIG. 2B

# Figure 3

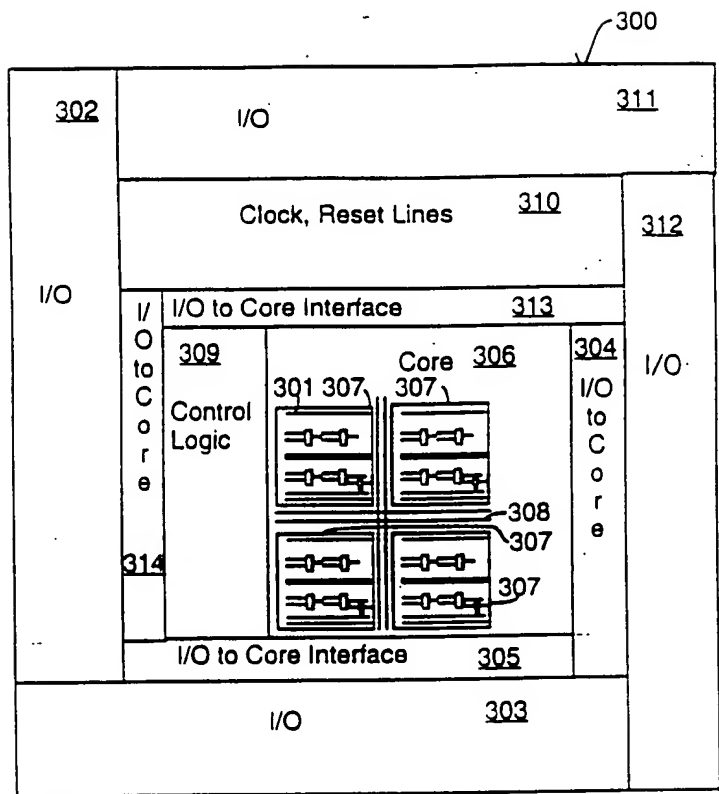


FIG. 4A

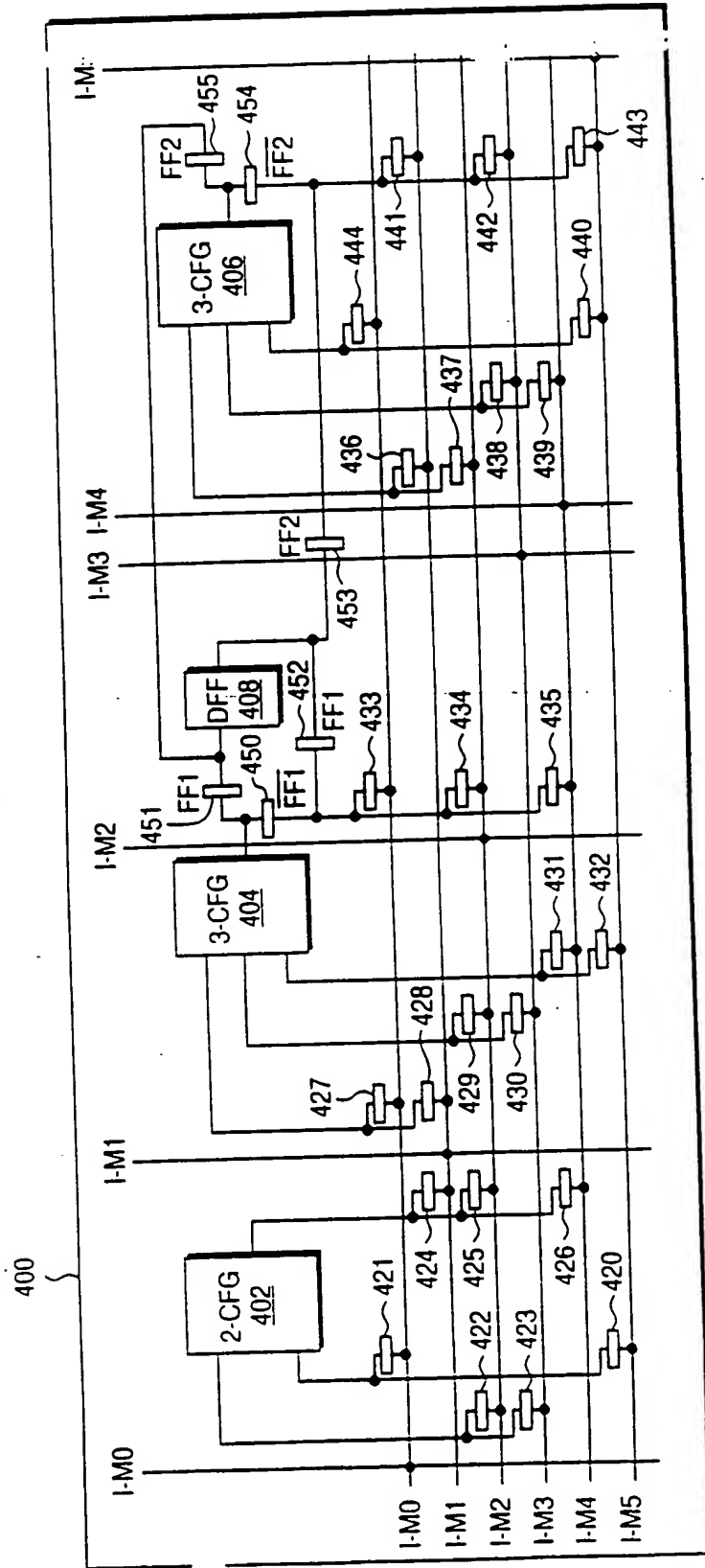




FIG. 4B

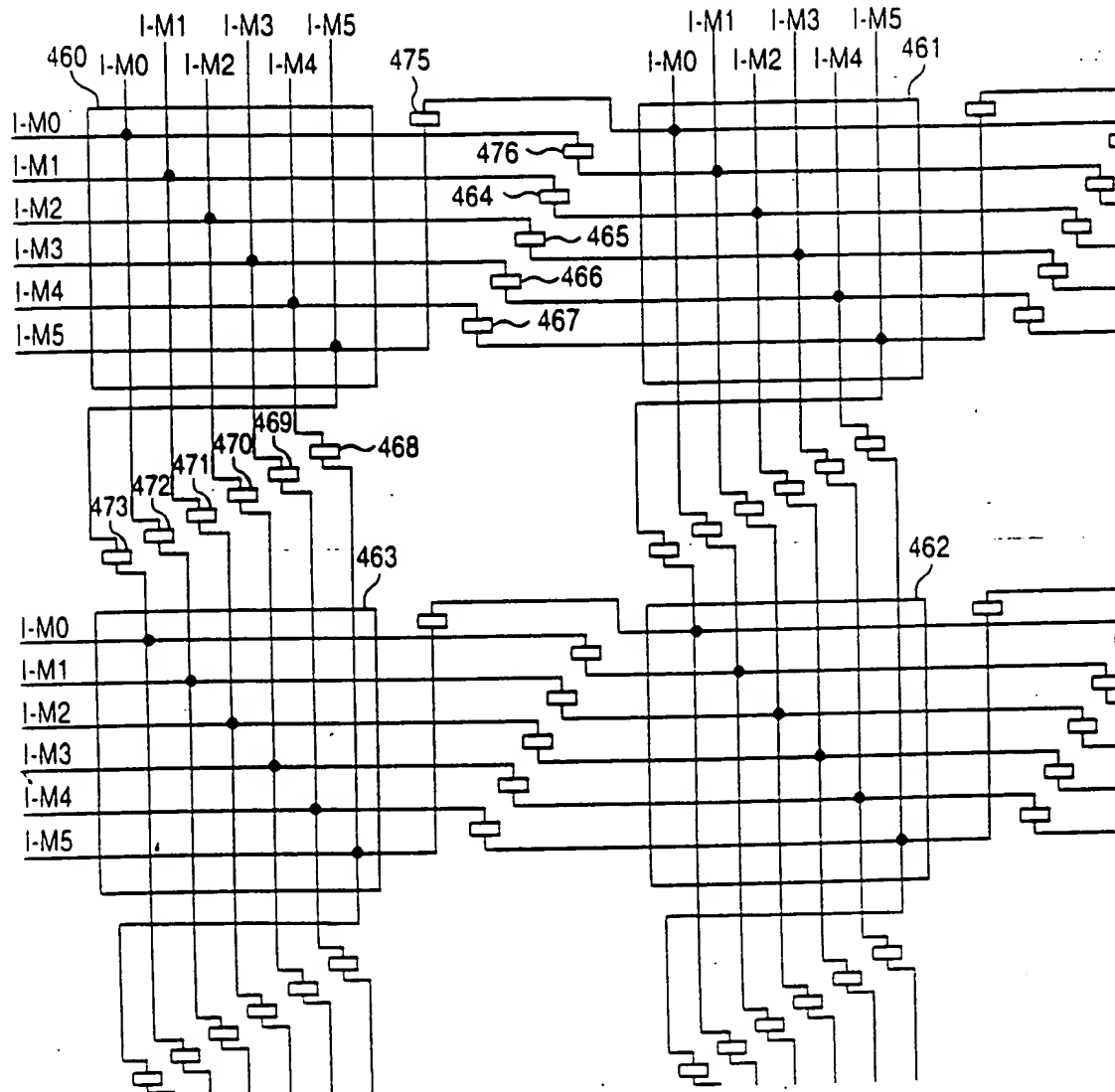


FIG. 5A

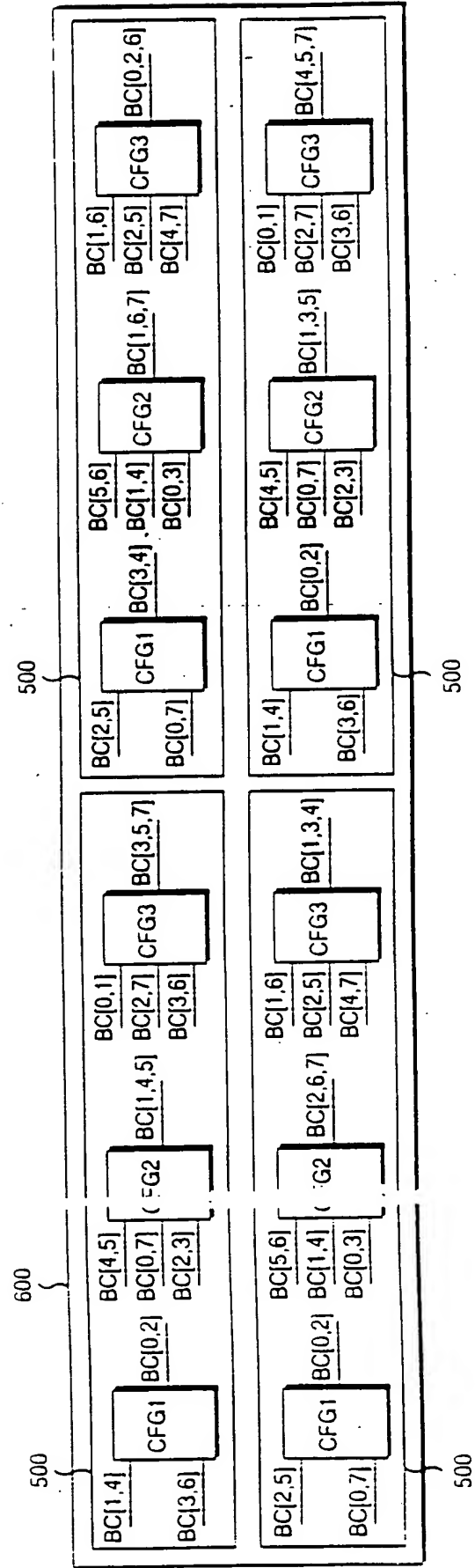


FIG. 5B

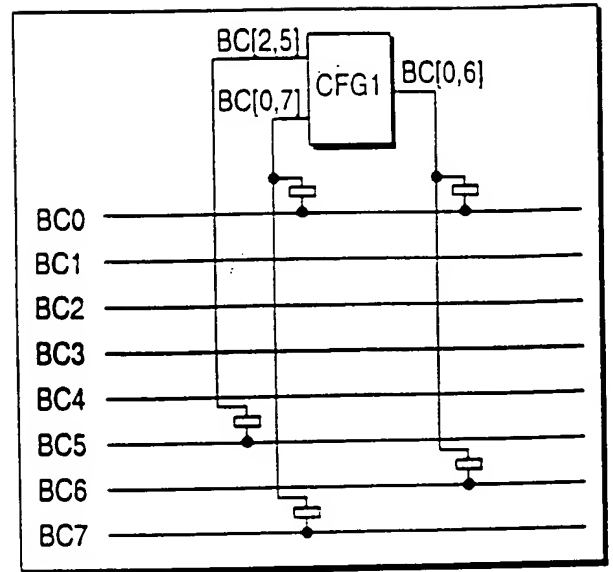
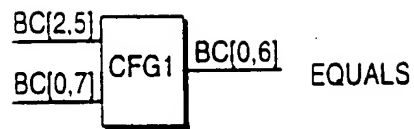


FIG. 6

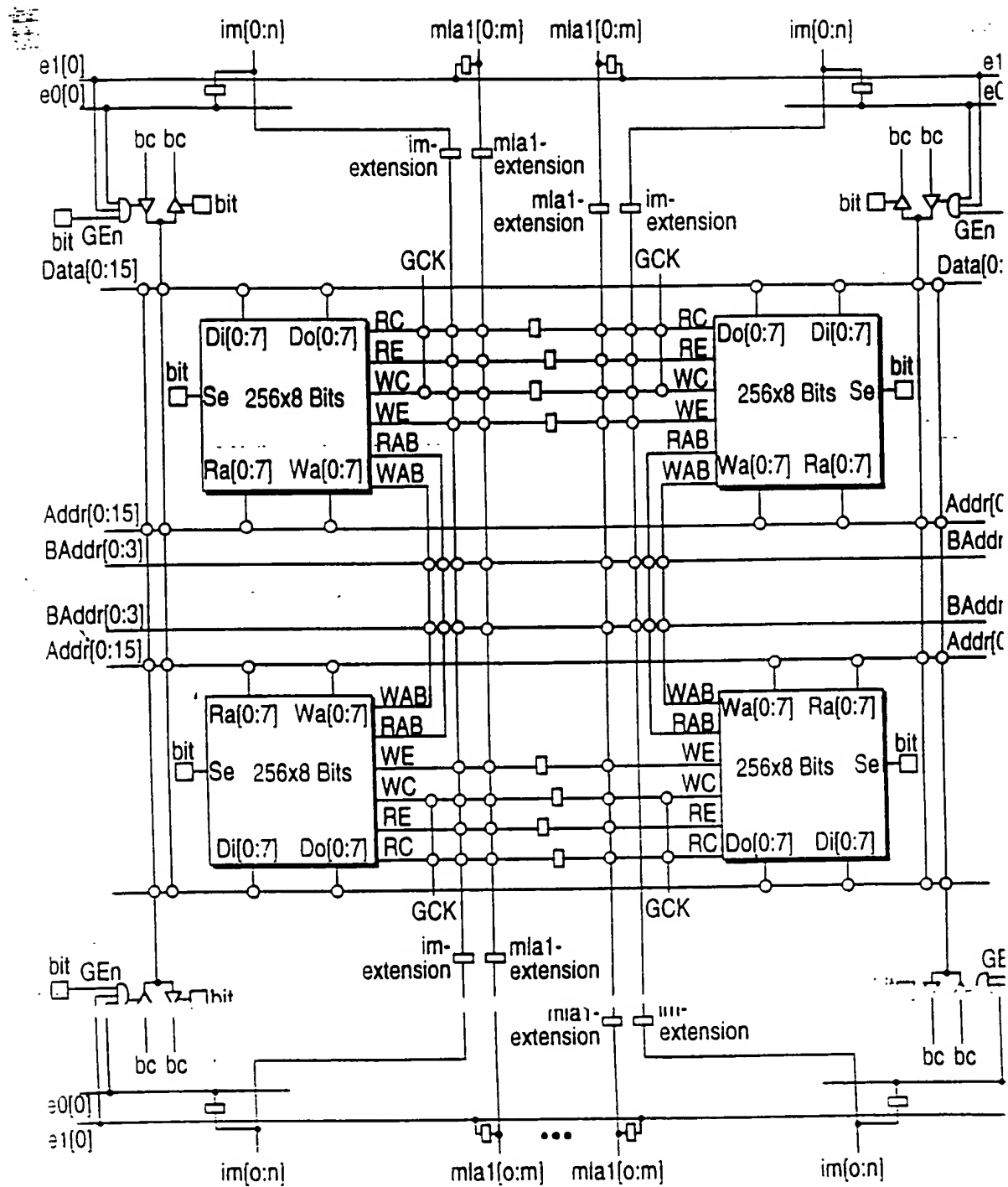


FIG. 7A

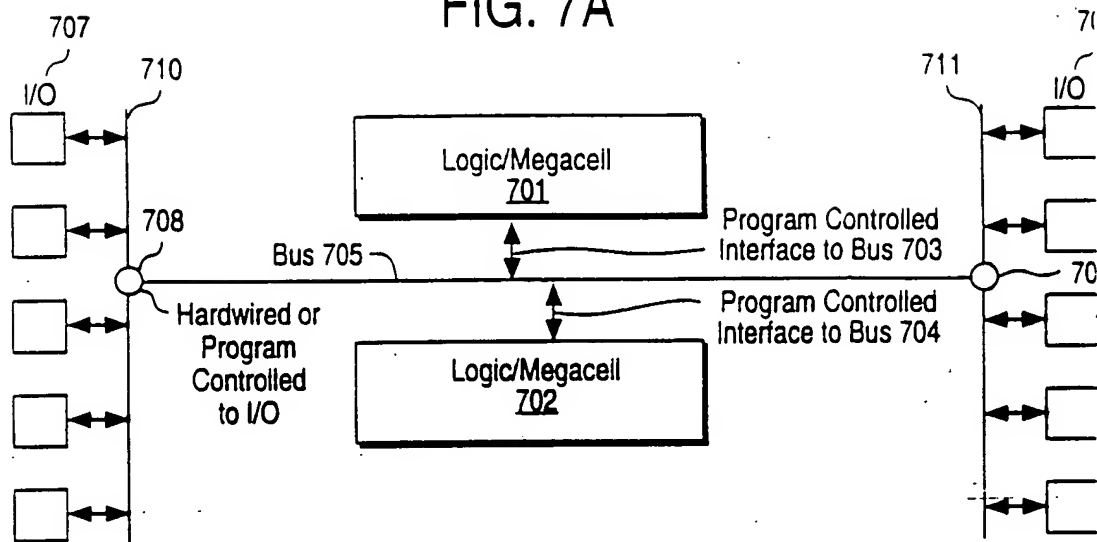


FIG. 7B

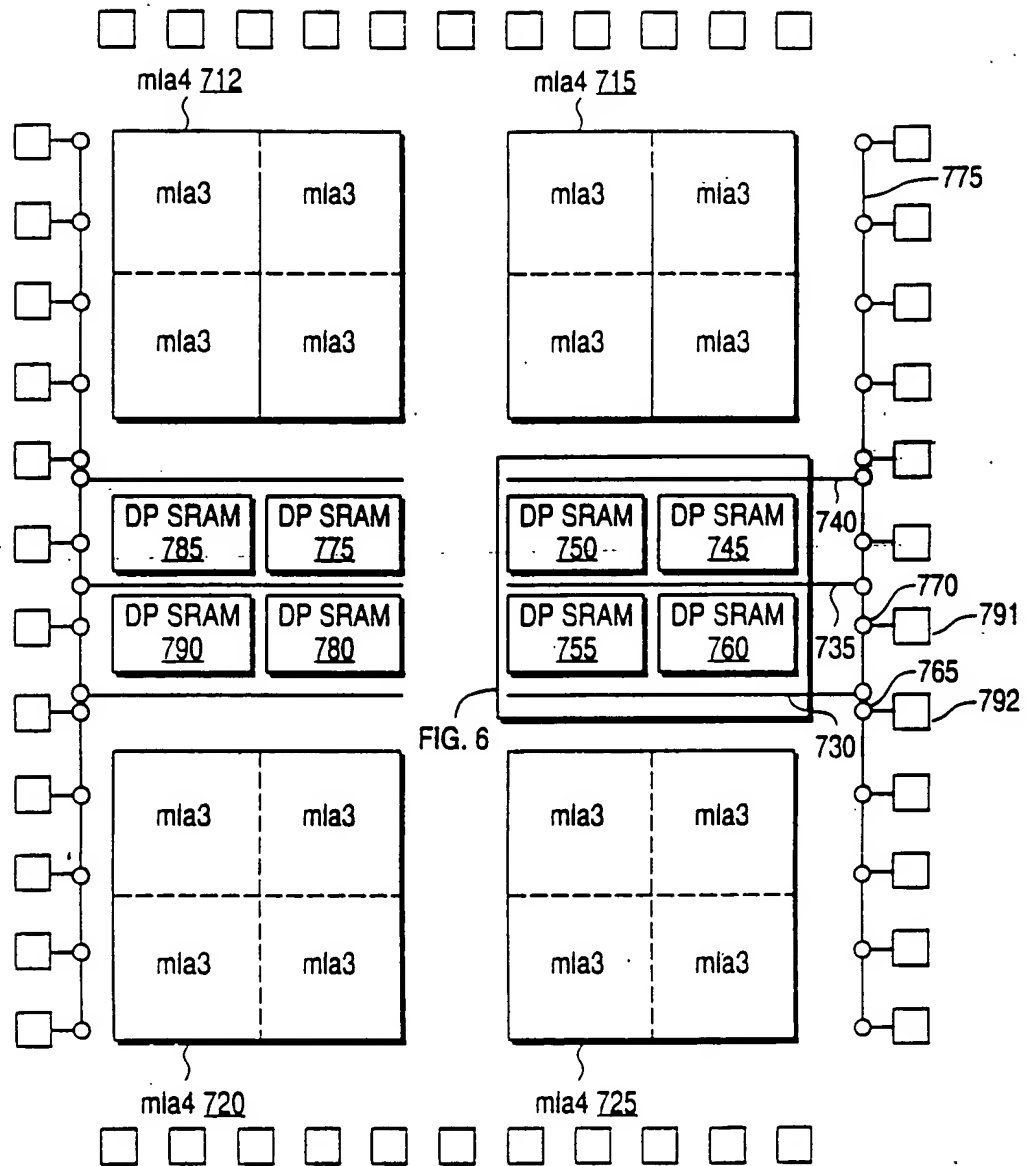
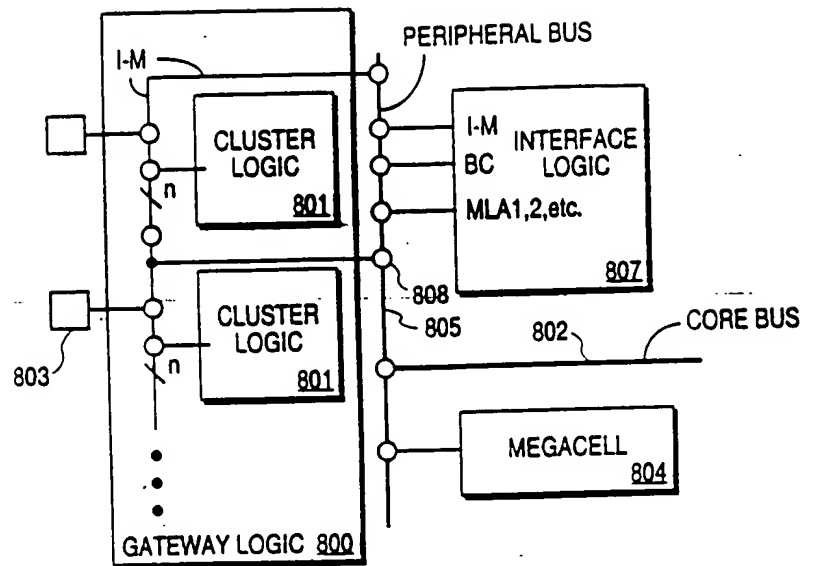
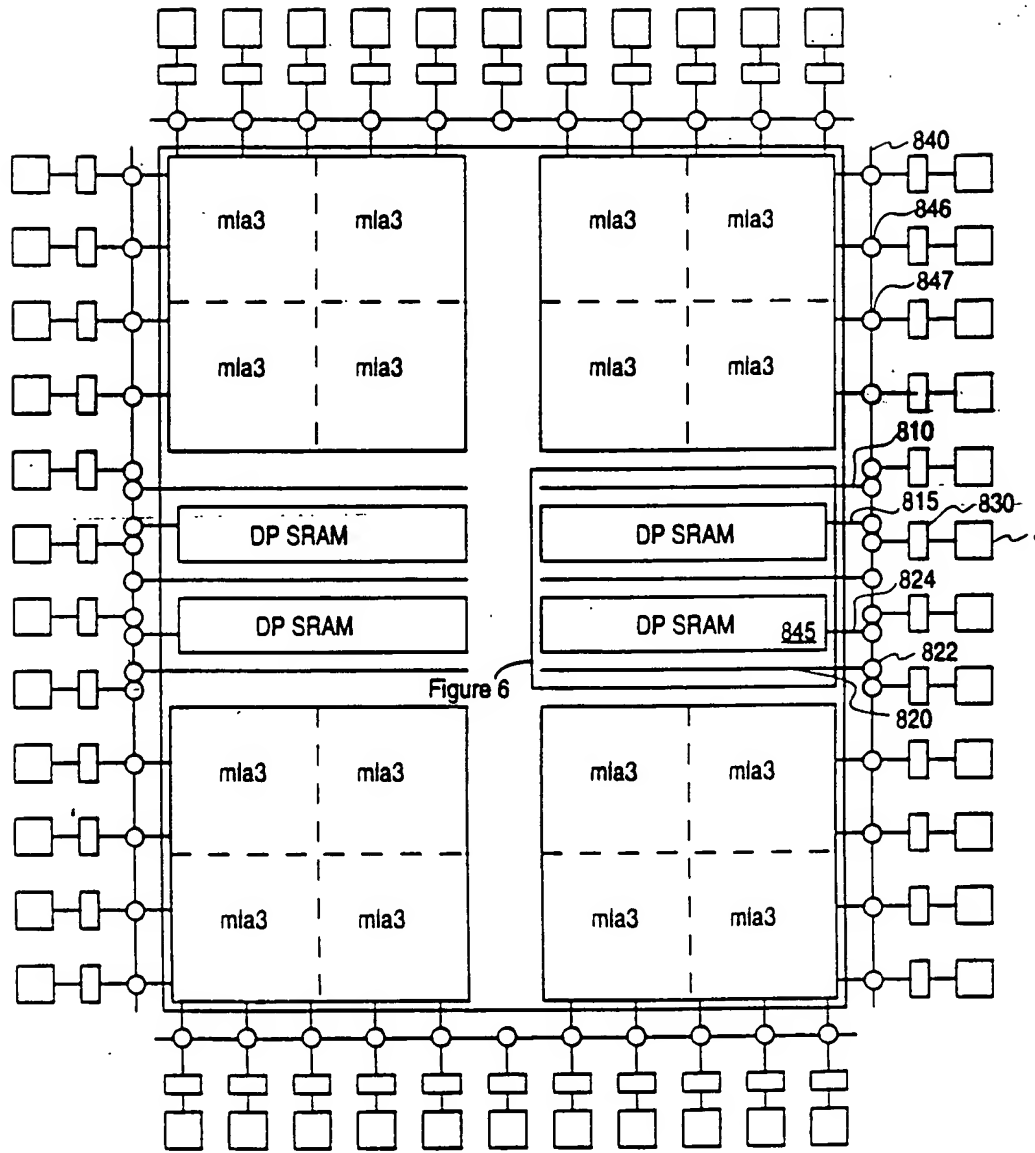


Figure 8A



○ : Each can be bi-directional , tri-statable

FIG. 8B



□ : Logic Cluster next to I/O



*This sheet  
 has been  
 replaced with  
 Figures 2A &  
 2B.*

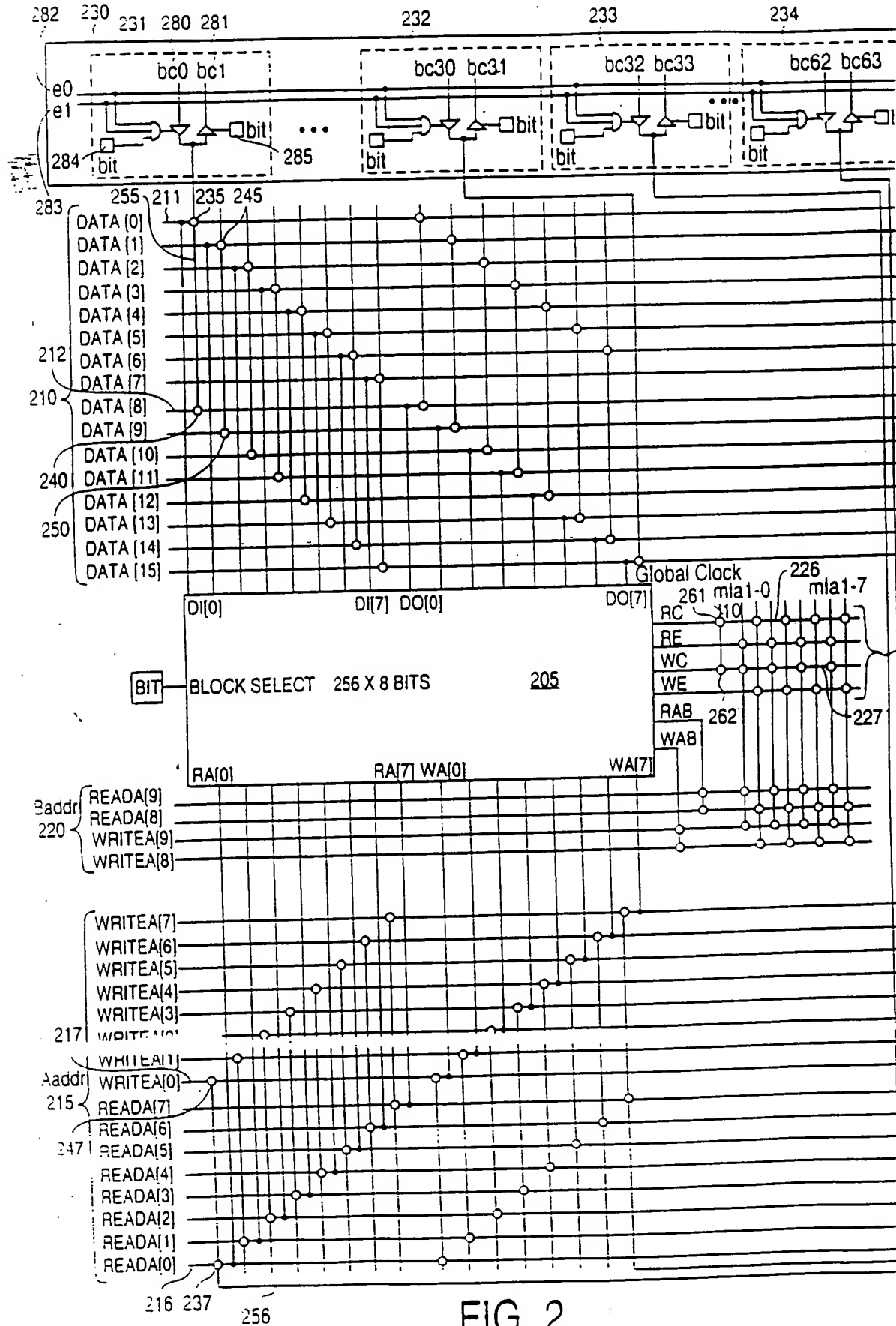


FIG. 2